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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/050,101	01/18/2002	Minoru Nakano	2002_0046A	6540

513 7590 07/28/2005
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WASHINGTON, DC 20006-1021

EXAMINER

CHAVIS, JOHN Q

ART UNIT PAPER NUMBER

2193

DATE MAILED: 07/28/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/050,101

Applicant(s)

NAKANO, MINORU

Examiner

John Chavis

Art Unit

2191

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 18 January 2002.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-19 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 4/15/02.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1-19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Criss et al. (2001/0029178) in view of Criss's suggestion of the typical use of update program placements in manufacturing systems.

What is claimed is:

Criss

1. A system for supplying a semiconductor manufacturing system control program comprising:

See the title and the abstract. Criss does not specifically indicate that his system is specifically for a semiconductor manufacturing system; however, he suggests that similar systems are used for tracking parts, completed products, defects, etc. in manufacturing system, see the last sentence in sect. 0003. Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention to utilize the feature in Criss's system for the same reasons listed above to enable updates in a manufacturing system with software that is outdated, see sect. 0002.

an administrative server;

See the "host computer" in sect. 0013.

a communications circuit connecting the administrative server to a controller

See sect. 0015.

of a semiconductor manufacturing system;

a facility provided in the controller for determining a time when a previously installed control program can be changed;

See the "schedule table" in sect. 0013.

and a facility provided in the controller responsive to a result of the determination for storing a control program received from the administrative server through the communications circuit in a memory to be executable by a processor.

See sect. 0016.

2. A system for supplying a semiconductor manufacturing system control program according to claim 1, wherein:

See sect. 0012.

a time when the semiconductor manufacturing system is performing a process event for growing a film on a substrate under processing is not determined as a time when the control program can be changed.

See the rejection of claim 1 above for claims 3 and 4.

Claim 5 is taught via sect. 0006 and sect. 0016.

As per claim 6, see again sect. 0006, "executed at any time", implies that the original program can be used instead.

In reference to claims 7-12, see the rejection of claims 1-6 above.

Claims 13-15 are rejected as claim 5 above.

As per claims 16-19, see the rejections of claim 6 above.

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to John Chavis whose telephone number is (571) 272-3720. The examiner can normally be reached on M-Th, 7:30am-4:00pm, EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kakali Chaki can be reached on (571) 272-3719. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jc

Handwritten signature of John Chavis, consisting of a stylized 'Jc' followed by a horizontal line.

John Chavis
Primary Examiner AU-2191